DENSiTRON

UREADY 2RU X86 CONTROL SURFACE

This universal touchscreen control surface is 2RU rack mountable, with an embedded X86 platform and Ethernet connectivity.

The display has 1920 x 285 pixels and an optically bonded capacitive touchscreen providing optimum optical quality. The display features wide-angle symmetric viewing making it easier to be used in many operational applications.

The embedded X86 platform is Densitron's single board computer based on Intel Apollo N4200 utilising Quad core 1.1GHz with burst frequency of 2.5 GHz.

Along with the four CPU cores, the chip integrates a reworked Intel HD Graphics 505 (Apollo Lake) GPU based on Intel's Gen9 architecture supporting DirectX 12.

Densitron's X86 architecture and software ecosystem allows customers to rapidly deploy their applications, thus getting products to market sooner. Both Linux and Windows 10 OS are supported.

UReady™
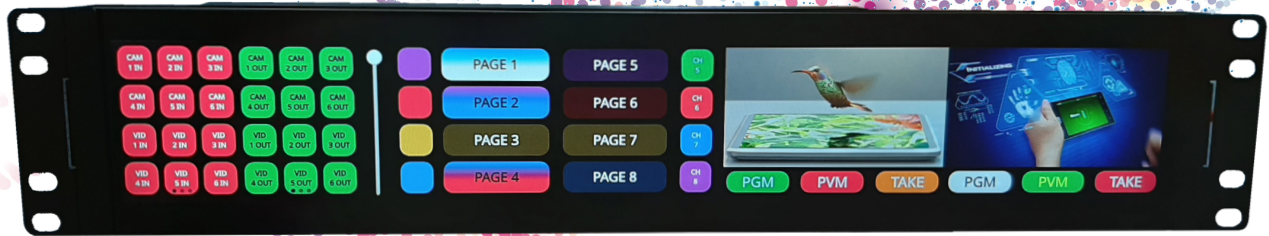
UREADY 2RU X86

CONTROL SURFACE

SKU: DM-163GN-MPXR03

Densitron 16.3" control surface with powered by X86.

DISPLAY

Display size	16.3"
Resolution	1920 x 285
Viewing Angle	80/80/80/80

DIMENSIONS

Width	483.20mm
Height	87.60mm

HMI

HMI	Standard Touch
Touch Interface	USB

MOUNTING AND CONNECTIVITY

I/O	Ethernet
Mechanical Mounting	Closed Frame

Available with the following customisation options:
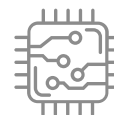
Cover Lens Customisation

Optical Bonding

Mechanical buttons

ARM Available

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COMPUTING

Processor	Intel® Pentium® Processor N Series (N4200) Quad core processor with 1.10 GHz
Memory	4 GB DDR3-1600
Storage	64 GB SSD M.2
Audio	Line-out(left/right channels) x 1, Mic-in x 1
Inputs	User USB x2
I/O External	Ethernet: 10/100/1000 Mbps x 1. IEEE 802.3af 12V DC IN Jack
Graphic Proccessor	Intel® HD Graphics 505 Graphics Base Frequency 200 MHz DirectX*, OpenGL*, Intel® Quick Sync Video

PANEL

Size	16.3" TFT LCD
Pixels per inch	119
Brightness	700 cd/m^2
Contrast Ratio	1000
LED Backlight	50,000 Hours
Power	1 x 12V DC input
Operating Temperature	0°C to +45°C

TOUCH

Type	PCT
Fingers	Fingers Multiple touch > 5

OPERATING SYSTEM

Windows	Available on request
Linux	Available on request

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